

**SREENIDHI INSTITUTE OF SCIENCE AND TECHNOLOGY**

**B.Tech - IV - Year - I Sem (A17 Regulations) Supplementary Examinations, May / June - 2025.**

**TIME TABLE**

**TIME ⇨ AN:-12:45 PM TO 03:45 PM**

BRANCH	DATE AND DAY							
	27-05-2025 Tuesday	28-05-2025 Wednesday	29-05-2025 Thursday	30-05-2025 Friday	31-05-2025 Saturday	02-06-2025 Monday	03-06-2025 Tuesday	04-06-2025 Wednesday
CIVIL ENGINEERING	Disaster Mitigation and Management (PE-V) IV-II	Ground Improvement Techniques (PE-III)	Estimation and Valuation	Finite Element Method for Civil Engineers	Concrete Technology	Structural Engineering Design & Detailing	Intellectual Property Rights	Pre-stressed Concrete Structures (PE-IV)
ELECTRICAL AND ELECTRONICS ENGINEERING	Management Science	Utilization of Electrical Energy	Reactive Power Control & Management (PE-V)	Power Semi Conductor Drives	Power System Analysis and Control	HVDC & FACTS (PE-III)	--	--
MECHANICAL ENGINEERING	Management Science	Jet Propulsion and Rocket Engineering(PE-V)	Power Plant Engineering(PE-IV)	RES/ Mechatronics (PE-III)	Metrology & Instrumentation	Refrigeration and Air Conditioning / AMP (PE-II)	Intellectual Property Rights	Finite Element Method
ELECTRONICS AND COMMUNICATION ENGINEERING	Computer Networks	VLSI Technology and Design	ERTS / MIMO OFDM/ FPGA Architecture And Application (PE-IV)	DDTV / PP / Satellite Communication (PE-III)	Liner Control Systems	--	Introduction to System Engineering / SCA / WCN (PE-V)	--
COMPUTER SCIENCE AND ENGINEERING	Information Security	Software Automation and Testing	Advanced Entrepreneurship (OE-III)	CC/ Artificial Intelligence and Deep Learning (PE-V)	Innovation and Design Thinking (OE-II)	Compiler Design	Intellectual Property Rights	--
INFORMATION TECHNOLOGY	Information Security	Software Automation and Testing	GME/ Advanced Entrepreneurship (OE-III)	ASE/ Artificial Intelligence and Deep Learning (PE-V)	Innovation and Design Thinking (OE-II)	Mobile Application Development	Intellectual Property Rights	--
ELECTRONICS AND COMPUTER ENGINEERING	Embedded and Real Time Systems*	VLSI Design	GME (OE-II)	Artificial Intelligence and Deep Learning (PE-IV)	Innovation and Design Thinking (OE-II)	Automata Theory and Compiler Design	Intellectual Property Rights	--
BIO-TECHNOLOGY	Management Science	Food Bio Technology (PE-III)	Downstream Processing	Animal Biotechnology	Mass Transger and Seperation	Cancer Biology (PE IV)	Industrial and Envirmental Bio Technology	--

**DATE:- 02-05-2025**

**ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY**

*Note: No Student will be allowed to the examination hall without college uniform, ID's and Hall- ticket. If any student carries mobile to the examinations hall, it will be confiscated.*

PP:-Python Programming (ECE)

RES:-Renewable Energy systems (ME)

CC:-Cloud Computing (CSE)

DDTV:-Digital Design Through Verilog (ECE)

WCN:-Wireless Communications and Networks (ECE)

SCA:-System on-Chip Architecture (ECE)

AMP:-Advanced Manufacturing Processes (ME)

GME:-Genreal Management and Entrepreneurship (IT,ECM)

ASE:-Advanced Software Engineering (IT)

*Controller of Examinations*

1 The Director / Principal for kind information

3 HoDs of CIVIL/EEE/ME/ECE/CSE/IT/ECM/CS/AI ML/DS/IOT/S&H with request to cirulate among the students and arrange to place a copy on the notice board.

3 The SAP Section with a request to send SMS to parents & Students and also place it in the website.

*Chief Superintendent of Examinations*

*o/c E. Murugan*